

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re application of : September 30, 1999
Bullis et al : IBM Corporation - 972/B656
: P.O. Box 12195
Serial No. : Research Triangle Park,
: North Carolina 27709
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For: Method and System for Providing Hierarchical Self-Checking in ASIC Simulation

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington DC 20231

Dear Sir:


This Information Disclosure Statement is being submitted in connection with the above-identified application for patent. Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 C.F.R. § 1.98(a)(1). A copy of each of the items identified on the attached Form PTO-1449 is supplied herewith.

Respectfully submitted,


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